

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

J1046 U.S. P1
10/068004
02/08/02
5C3-P83

APPL NUM 10068004	FILING DATE 02/08/2002	CLASS 257	SUBCLASS --	GAU 2811	EXAMINER <i>Serge</i>
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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-10842 03/02/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO 1514.1010		

TITLE : Thin film transistor and manufacturing method thereof, and active matrix display device and manufacturing method thereof

U.S. DEPT. OF COMM/PAT & TMD/PTO-45EL (Rev. 12-92)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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